## Germanium-based virtual substrates for low cost III-V on Si tandem solar cell

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Efficiency records for single and multijunction solar cells rely on III-V materials such as GaAs. Nevertheless, single junction GaAs based solar cells are 200 times more expensive to produce than Si based solar cells.<sup>1</sup> This is mostly due to the cost of substrates used for GaAs deposition: GaAs and Ge. Therefore, replacing the usual substrates with a cheaper one could be a way to reduce the cost of those solar cells. Moreover, to have a tandem solar cell, it would be interesting for the substrate to act as a bottom cell. Silicon is a cheap material which can also act as a bottom cell for tandem devices. So, an attractive option is to use silicon as both a substrate for GaAs deposition and bottom cell for tandem device.

Nevertheless, the lattice mismatch between GaAs and Si ( $\Delta a_{Si/Ge} = 4,0\%$ ) induces important strains and dislocations in the deposited GaAs. Dislocations are preferential areas for hole-electron pairs to recombine, resulting in a decrease of the solar cell efficiency. Therefore, direct growth of good quality, dislocation-free GaAs layer on Si is complicated. Some direct growths are made using gradient buffer layer, like GalnP/GaAs/Si with AlGaAsP gradient buffer layer. Nevertheless, the buffer layers is more than 2 µm thick, which leads to important material consumption and cost, interferences with optical properties such as absorption. Moreover, efficiency of this device is 25.9% under AM1.5g conditions, 14% less than III-V multijunction efficiency record under same conditions.<sup>2</sup>

For many years, PECVD deposition of Ge layers on Si has been studied at LPICM.<sup>3</sup> Latest results show Ge layers with low dislocation density: 5.10<sup>6</sup> cm<sup>-2</sup>.<sup>4</sup> In the light of these results, this project aims to develop direct growth of GaAs on a virtual substrate made of an ultrathin Ge layer deposited on Si.

To determine the deposition parameters of Ge on Si in a PECVD reactor leading to the best Ge quality, an analysis of impact of thickness, deposition temperature and substrate properties has been conducted. Moreover, the deposition of SiGe on Si has been studied for further implementation of a gradient SiGe layer in the virtual substrate. Finally, a technique to measure the TDD in Ge layer must be studied as it is a critical criterion to evaluate the layers quality.



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- <sup>2</sup> Feifel, M., Lackner, D., Schön, J., Ohlmann, J., Benick, J., Siefer, G., Predan, F., Hermle, M. and Dimroth, F. (2021), Epitaxial GalnP/GaAs/Si Triple-Junction Solar Cell with 25.9% AM1.5g Efficiency Enabled by Transparent Metamorphic Al<sub>x</sub>Ga<sub>1-x</sub>As<sub>y</sub>P<sub>1-y</sub> Step-Graded Buffer Structures. Sol. RRL, 5: 2000763. <u>https://doi.org/10.1002/solr.202000763</u>
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- <sup>4</sup> Romain Cariou, P. R. (2014). *Croissance épitaxiale de Si(Ge) sur Si et GaAs à basse température par PECVD, pour cellules solaires tandem*. Thèse de doctorat, Ecole polytechnique, Palaiseau. Retrieved from <u>https://theses.fr/2014EPXX0065</u>